



Analog circuit design techniques at 0.5V /

Chatterjee, Shouri

Springer,
2007

Electronic books

Monografía

Analog design at ultra-low supply voltages is an important challenge for the semiconductor research community and industry. Analog Circuit Design Techniques at 0.5V covers challenges for the design of MOS analog and RF circuits at a 0.5V power supply voltage. All design techniques presented are true low voltage techniques - all nodes in the circuits are within the power supply rails. The circuit implementations of body and gate input fully differential amplifiers are also discussed. These building blocks enable us to build continuous-time filters, track-and-hold circuits, and continuous-time sigma delta modulators. Current books on low voltage analog design typically cover techniques for supply voltages down to approximately 1V. This book presents novel ideas and results for operation from much lower supply voltages and the techniques presented are basic circuit techniques that are widely applicable beyond the scope of the presented examples. Analog Circuit Design Techniques at 0.5V is written for analog circuit designers and researchers as well as graduate students studying semiconductors and integrated circuit design

<https://rebiunoda.pro.baratznet.cloud:38443/OpacDiscovery/public/catalog/detail/b2FpOmNlbGVicmF0aW9uOmVzLmJhemF0ei5yZW4vMjIwNTg1ODk>

Título: Analog circuit design techniques at 0.5V Shouri Chatterjee [and others]

Editorial: New York Springer 2007

Descripción física: 1 online resource (xi, 156 pages) illustrations

Mención de serie: Analog circuits and signal processing series 0893-3405

Bibliografía: Includes bibliographical references (pages 147-153) and index

Contenido: Preliminary; Introduction; Fully Differential Operational Transconductance Amplifiers (OTAs); Weak Inversion MOS Varactors for Tunable Integrators; A 0.5 V 5th-Order Low-Pass Elliptic Filter; A 0.5 V Track-and-Hold (T/H) Circuit; A 0.5 V Continuous-Time Modulator; 0.5 V Receiver Front-End Circuits; Back matter

Lengua: English

Copyright/Depósito Legal: 647912136 655854038 746931709 767242824 821876550 994767311 1005809427 1035709424 1040647164 1058407817 1066628819 1069523231 1078845028 1078855859

ISBN: 9780387699547 0387699546 9780387699530 hd. bd.) 0387699538 hd. bd.) 1282823493 9781282823495 9786612823497 6612823496

Materia: Linear integrated circuits- Design and construction Electronic circuit design Ingénierie. Electronic circuit design. Linear integrated circuits- Design and construction. Anologschaltung. Schaltungsentwurf. Engineering Circuits and Systems Signal, Image and Speech Processing Electronics and Microelectronics, Instrumentation Energy Technology Energy, general

Autores: Chatterjee, Shouri

Enlace a formato físico adicional: Print version Chatterjee, Shouri. Analog circuit design techniques at 0.5V. New York ; Berlin : Springer ; Dordrecht : Springer, 2007 9780387699530 (DLC) 2007924348 (OCoLC)123434586

Punto acceso adicional serie-Título: Analog circuits and signal processing series

Baratz Innovación Documental

- Gran Vía, 59 28013 Madrid
- (+34) 91 456 03 60
- informa@baratz.es