



# CMOS single chip fast frequency hopping synthesizers for wireless multi-gigahertz applications : design methodology, analysis, and implementation /

Bourdi, Taoufik

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Monografía

"In this book the authors outline detailed design methodology for fast frequency hopping synthesizers for RF and wireless communications applications. Great emphasis on fractional-N delta-sigma based phase locked loops from specifications, system analysis and architecture planning to circuit design and silicon implementation."--Jacket

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