



# Integrated Circuit and System Design. Power and Timing Modeling, Optimization and Simulation [ 18th International Workshop, PATMOS 2008, Lisbon, Portugal, September 10-12, 2008. Revised Selected Papers /

Svensson, Lars

Springer Berlin Heidelberg,  
2009

Computer science   Memory management (Computer science)   Logic design  
 Computer system performance   Systems engineering   Computer Science  
 Logic Design   Processor Architectures   System Performance and Evaluation  
 Arithmetic and Logic Structures   Memory Structures   Circuits and Systems

Monografía

This book constitutes the thoroughly refereed post-conference proceedings of 18th International Workshop on Power and Timing Modeling, Optimization and Simulation, PATMOS 2008, featuring Integrated Circuit and System Design, held in Lisbon, Portugal during September 10-12, 2008. The 31 revised full papers and 10 revised poster papers presented together with 3 invited talks and 4 papers from a special session on reconfigurable architectures were carefully reviewed and selected from numerous submissions. The papers are organized in topical sections on low-leakage and subthreshold circuits, low-power methods and models, arithmetic and memories, variability and statistical timing, synchronization and interconnect, power supplies and switching noise, low-power circuits; reconfigurable architectures, circuits and methods, power and delay modeling, as well as power optimizations addressing reconfigurable architectures

<https://rebiunoda.pro.baratznet.cloud:28443/OpacDiscovery/public/catalog/detail/b2FpOmNlbGVicmF0aW9uOmVzLmJhcmF0ei5yZW4vOTE3OTg2Nw>

**Título:** Integrated Circuit and System Design. Power and Timing Modeling, Optimization and Simulation [Recurso electrónico] 18th International Workshop, PATMOS 2008, Lisbon, Portugal, September 10-12, 2008. Revised Selected Papers edited by Lars Svensson, Jos Monteiro

**Editorial:** Berlin, Heidelberg Springer Berlin Heidelberg 2009

**Descripción física:** digital

**Mención de serie:** Lecture Notes in Computer Science 0302-9743 5349

**Documento fuente:** Springer eBooks

**Restricciones de acceso:** Acceso restringido a miembros del Consorcio de Bibliotecas Universitarias de Andalucía

**Detalles del sistema:** Modo de acceso: World Wide Web

**Fuente de adquisición directa:** Springer (CS)

**ISBN:** 9783540959489 978-3-540-95948-9 9783540959472 ed. impresa)

**Autores:** Monteiro, Jos

**Entidades:** SpringerLink (Online service)

---

### **Baratz Innovación Documental**

- Gran Vía, 59 28013 Madrid
- (+34) 91 456 03 60
- [informa@baratz.es](mailto:informa@baratz.es)